FIG.1

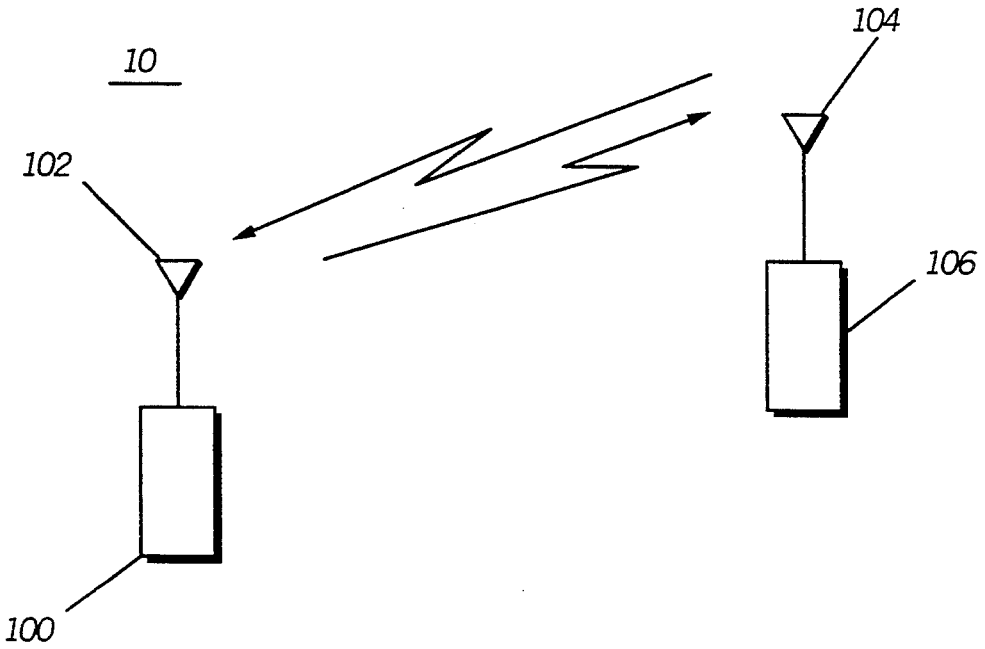
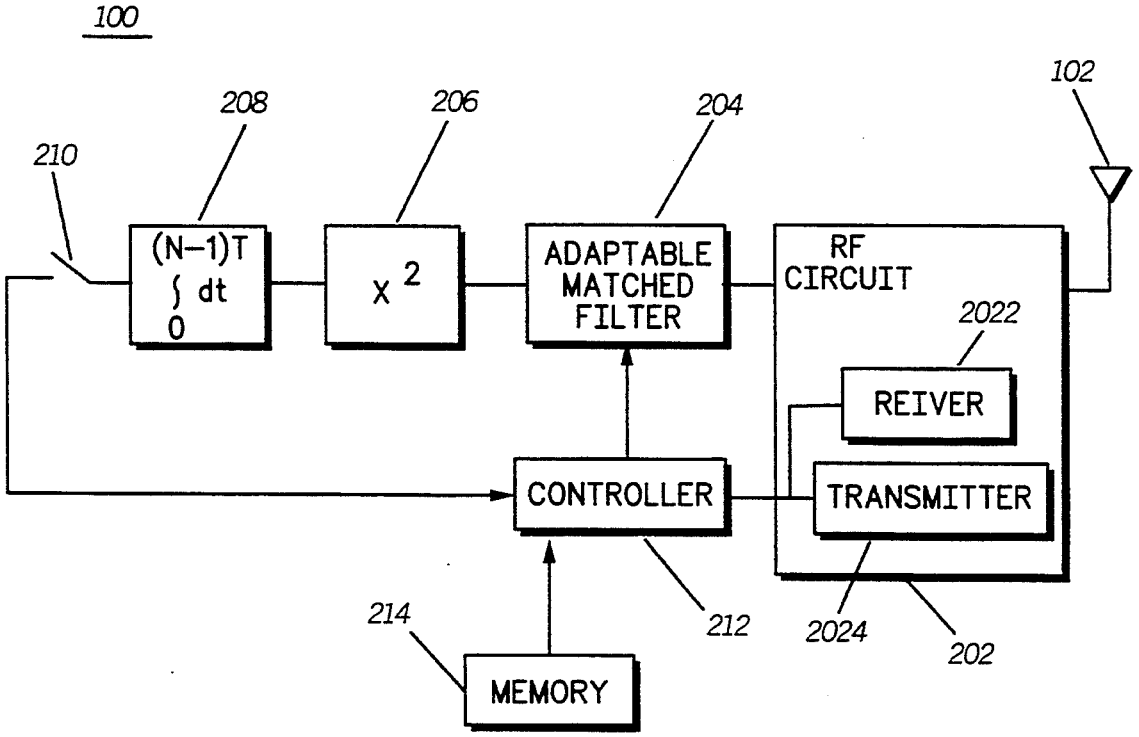

10
102
104
106
100

FIG.2


100
210
208
206
204
102
(N−1)T
∫ dt
0
x 2
ADAPTABLE
MATCHED
FILTER
RF
CIRCUIT
2022
REIVER
CONTROLLER
TRANSMITTER
214
212
2024
202
MEMORY