

*FIG.1*

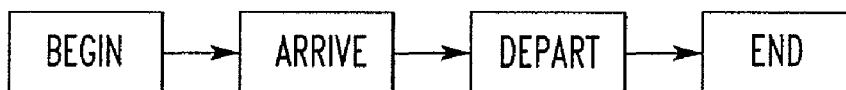
NEW PROCESS I : TRANSFER INPUT BUFFER



NEW PROCESS II : TRANSFER PROCESS



NEW PROCESS III : TRANSFER OUTPUT BUFFER

*FIG.2*