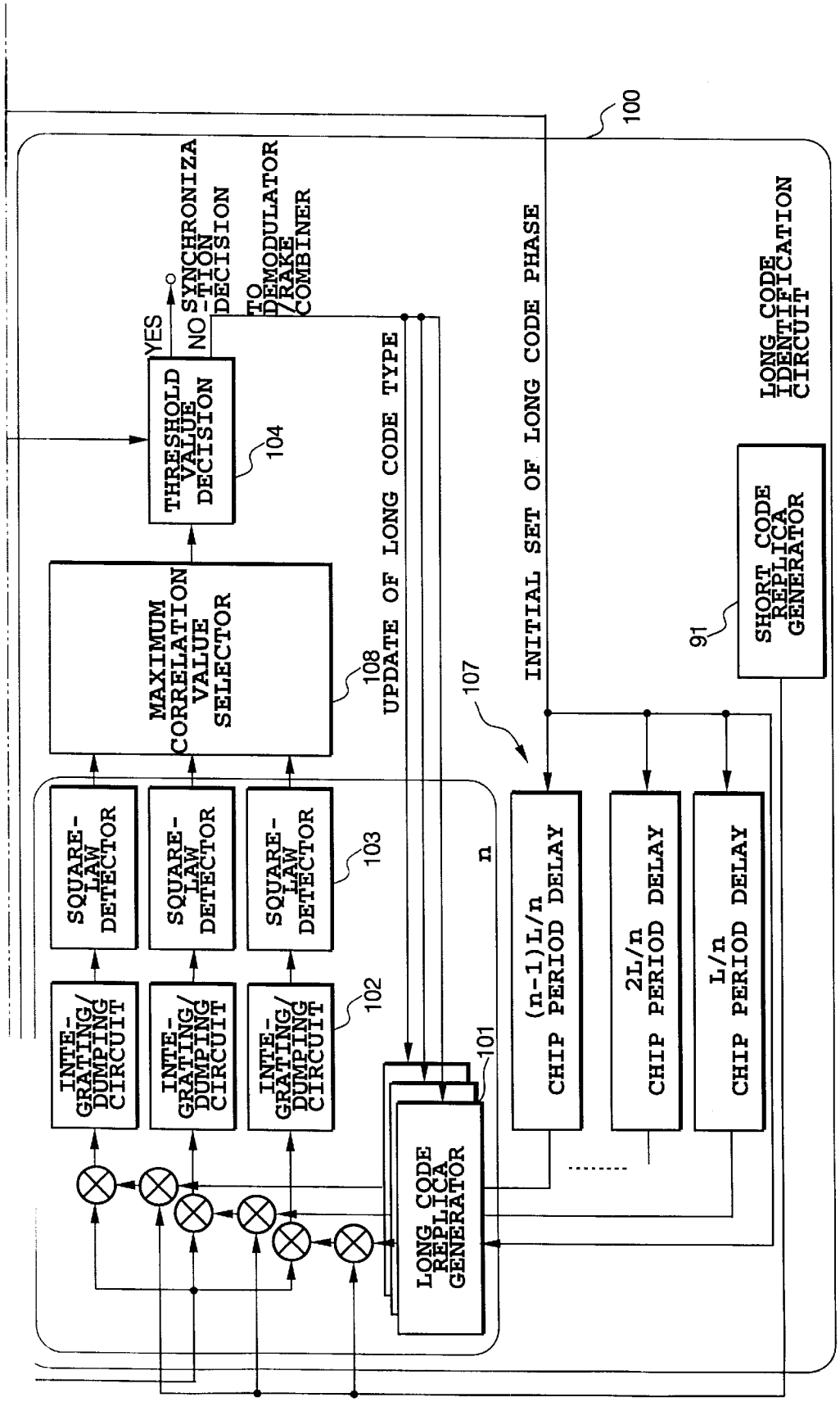
100
YES
NO
SYNCHRONIZA-TION DECISION
TO DEMODULATOR /RAKE COMBINER
THRESHOLD VALUE DECISION
104
MAXIMUM CORRELATION VALUE SELECTOR
108
UPDATE OF LONG CODE TYPE
107
INITIAL SET OF LONG CODE PHASE
LONG CODE IDENTIFICATION CIRCUIT
SHORT CODE REPLICA GENERATOR
91
SQUARE-LAW DETECTOR
SQUARE-LAW DETECTOR
SQUARE-LAW DETECTOR
103
n
INTE-GRATING/DUMPING CIRCUIT
INTE-GRATING/DUMPING CIRCUIT
INTE-GRATING/DUMPING CIRCUIT
102
101
LONG CODE REPLICA GENERATOR
(n-1)L/n CHIP PERIOD DELAY
2L/n CHIP PERIOD DELAY
L/n CHIP PERIOD DELAY

FIG.27B