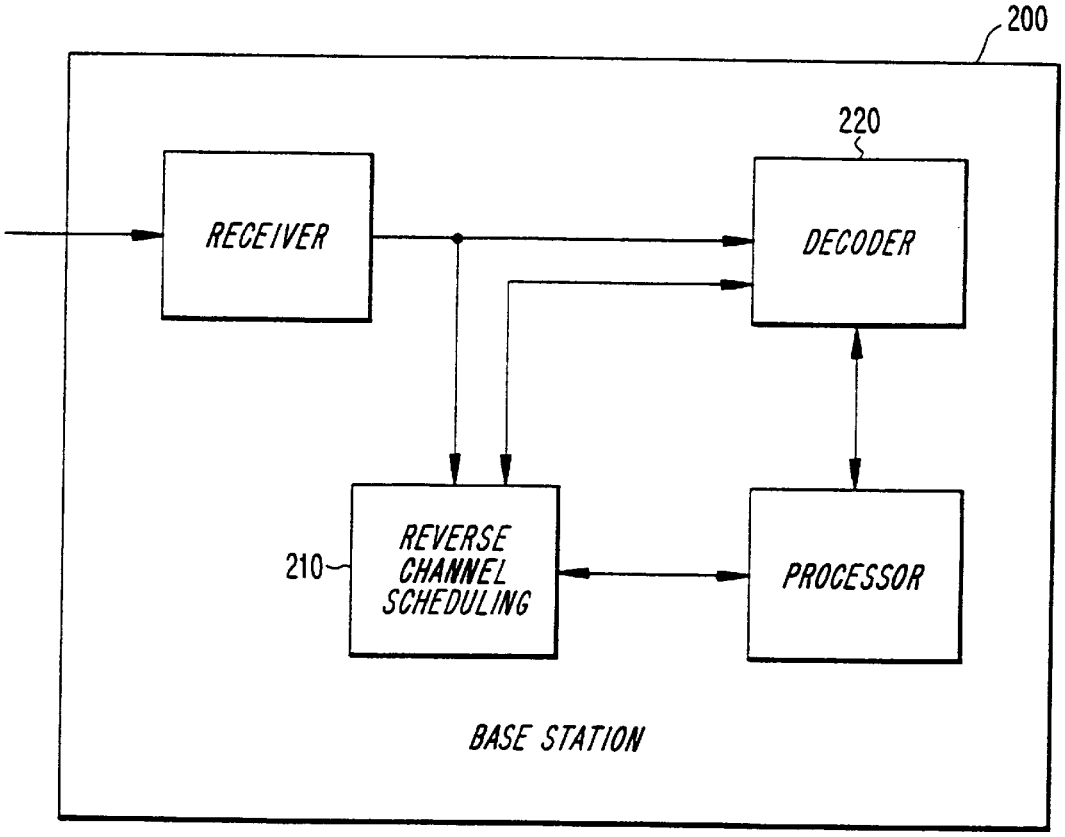
FIG. 12


200
220
RECEIVER
DECODER
210
REVERSE CHANNEL SCHEDULING
PROCESSOR
BASE STATION