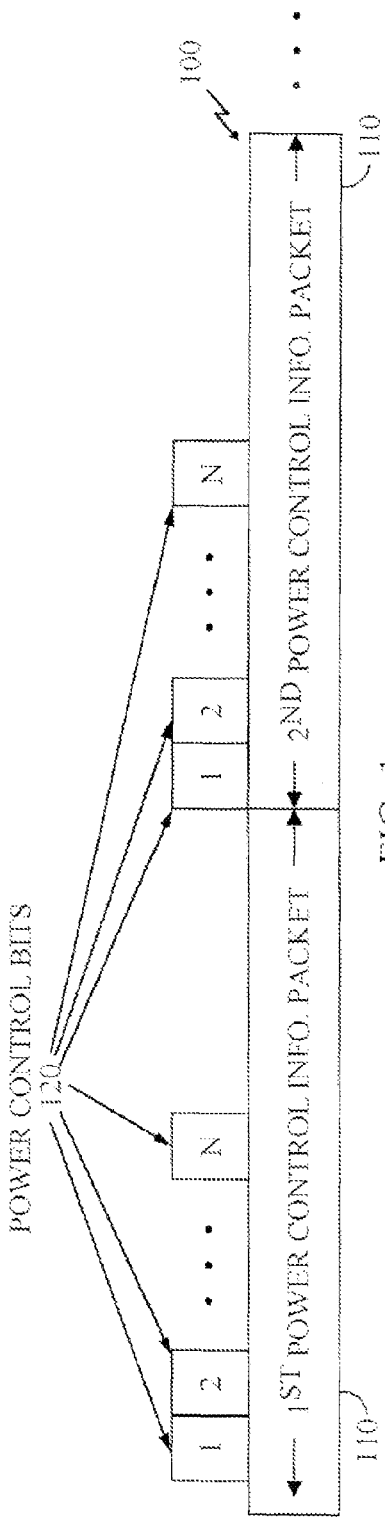
100
POWER CONTROL BITS
120
1
2
N
1ST POWER CONTROL INFO. PACKET
110
2ND POWER CONTROL INFO. PACKET
110

FIG. 1

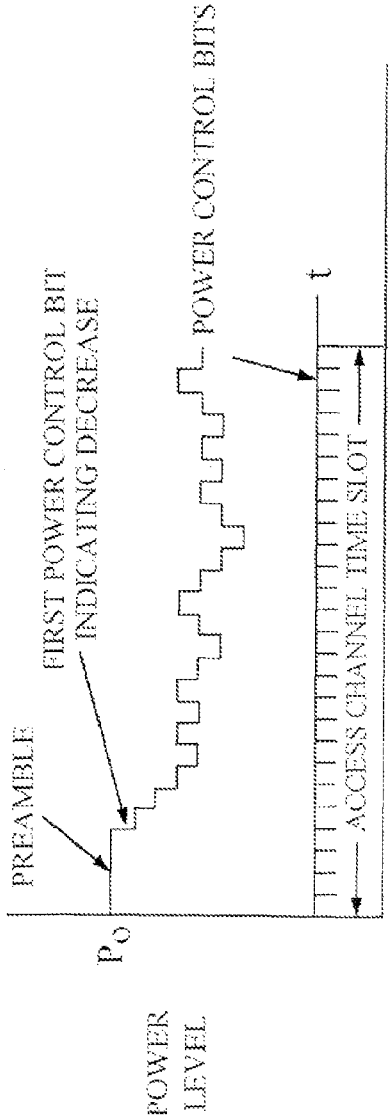
PREAMBLE
FIRST POWER CONTROL BIT
INDICATING DECREASE
POWER CONTROL BITS
P_0
POWER
LEVEL
t
ACCESS CHANNEL TIME SLOT

FIG. 2