

17

9. A display information conversion apparatus according to claim 8, wherein said video image converter includes a buffer memory for storing bit-parallel data in a digital manner, and write circuitry for converting video image information for said first specification into a form capable of being stored in said buffer memory, and for writing the same into said buffer memory,

18

said transfer circuitry includes read circuitry for reading said bit-parallel data from said buffer memory, and said second information processing apparatus converts said bit-parallel data into a video image signal for displaying a video image on said raster-scan display.

* * * * *