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[54] SPEECH RECOGNITION SYSTEM INCLUDING INTERRUPT SCHEME THAT AVOIDS OPERATIONAL AND INTERFACING CONFLICTS

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[52] **U.S. Cl.** **395/2**

[58] **Field of Search** 381/41–45; 395/2

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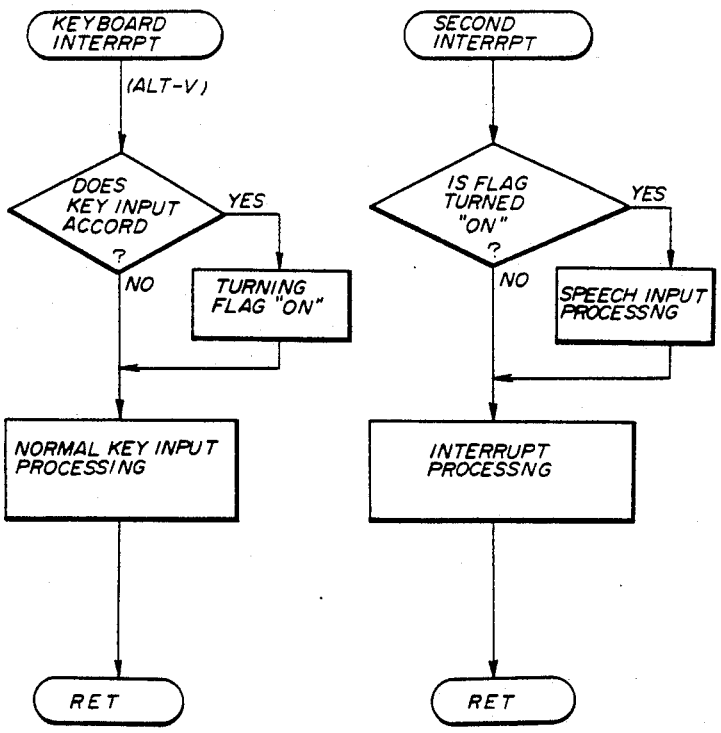
[57] **ABSTRACT**

A speech recognition system in which speech information inputted from a microphone is recognized and a speech signal corresponding to the speech information is generated and converted into a set of characters equivalent to keystroke information normally inputted from a keyboard, the set of characters being supplied to a keyboard buffer, so that an application program running on a computer system executes a desired job. The speech recognition system comprises a first interrupt processing part which, after receiving an interrupt signal due to a depression of a key on the keyboard, checks that information within the keyboard buffer accords with a first predefined keystroke code, turns a first flag ON only when the information accords with the first predefined keystroke code and terminates a first interrupt procedure, and a second interrupt processing part which, after the first interrupt routine is terminated, enables a second interrupt into operation of the application program only when the first flag is turned ON, so that a speech information input procedure is started to provide the keystroke information thus recognized.

8 Claims, 6 Drawing Sheets