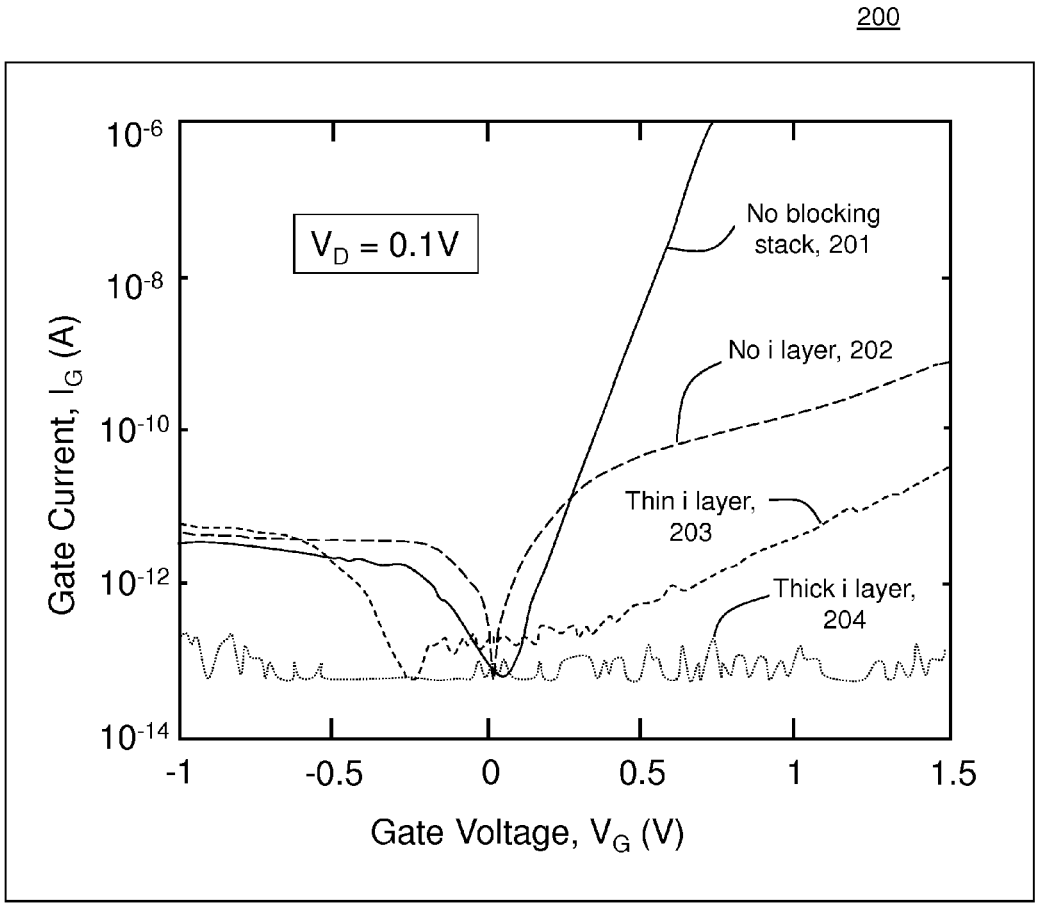
200
V_D = 0.1V
No blocking stack, 201
No i layer, 202
Thin i layer, 203
Thick i layer, 204
Gate Current, I_G (A)
10^{-6}
10^{-8}
10^{-10}
10^{-12}
10^{-14}
-1
-0.5
0
0.5
1
1.5
Gate Voltage, V_G (V)

FIG. 2