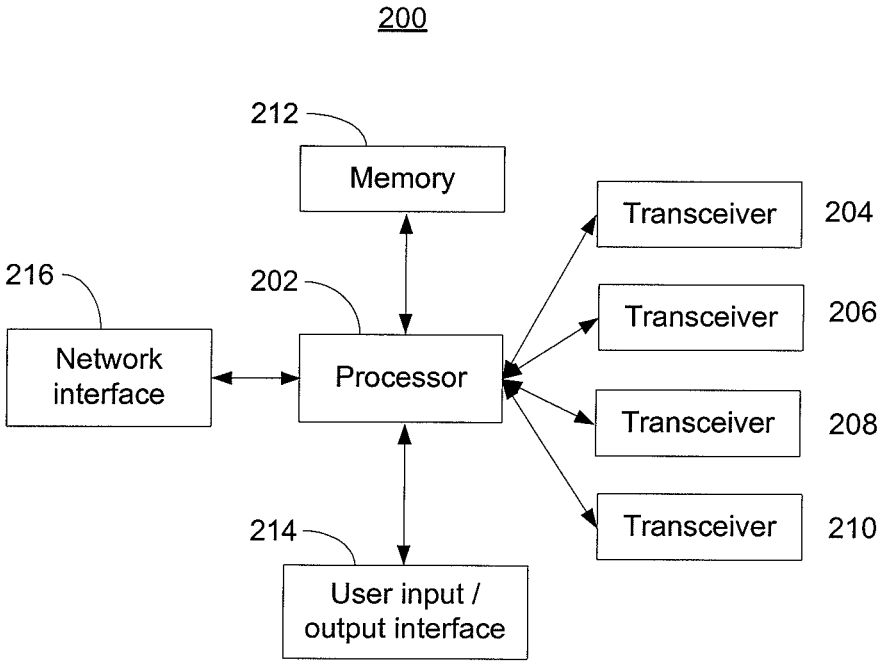
200
212
Memory
Transceiver
204
216
202
Transceiver
206
Network interface
Processor
Transceiver
208
Transceiver
210
214
User input / output interface

FIGURE 2