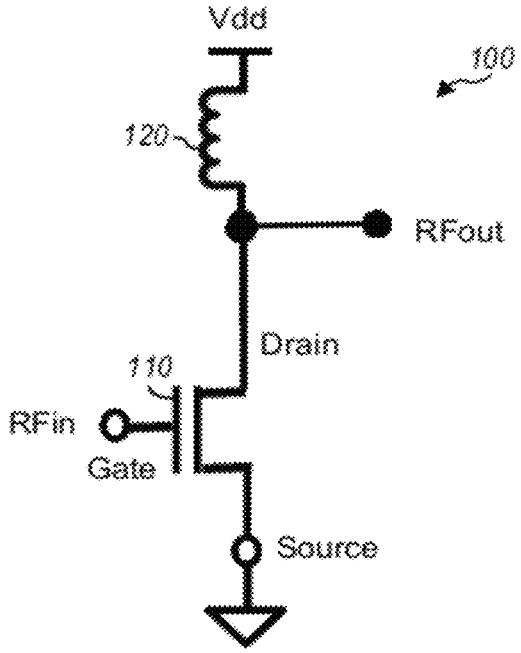
Vdd
100
120
RFout
Drain
110
RFin
Gate
Source

FIG. 1

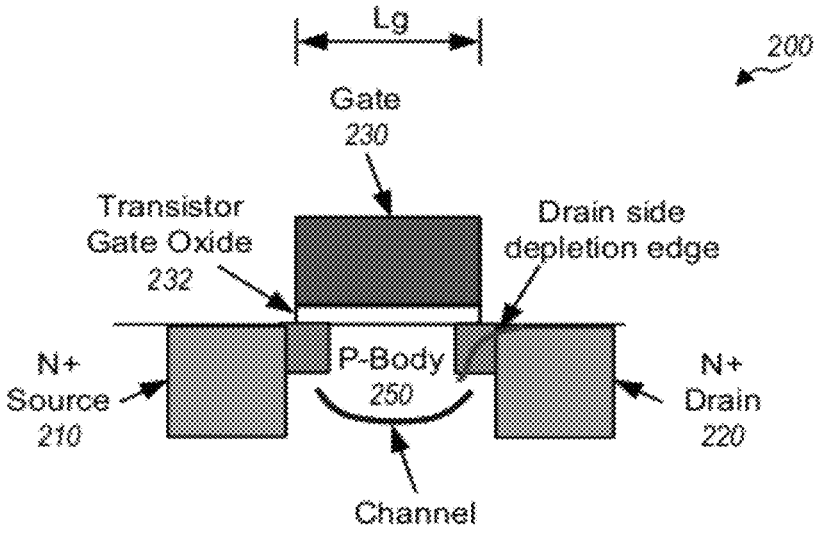
Lg
200
Gate
230
Transistor
Gate Oxide
232
Drain side
depletion edge
N+
Source
210
P-Body
250
N+
Drain
220
Channel

FIG. 2