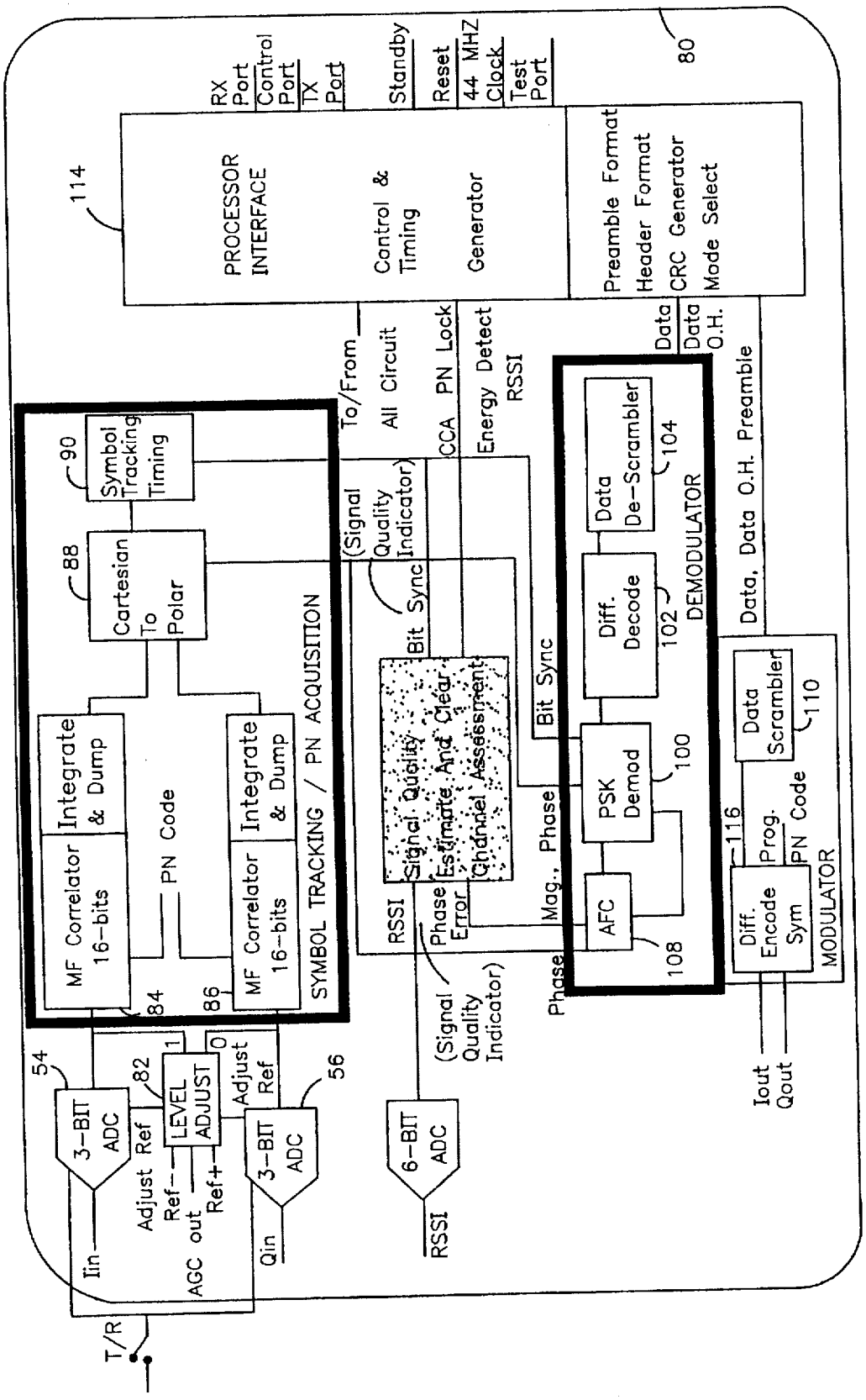
80
114
PROCESSOR INTERFACE
RX Port
Control Port
TX Port
Standby
Reset
44 MHZ Clock
Test Port
Control & Timing Generator
Preamble Format
Header Format
CRC Generator
Mode Select
To/From All Circuit
CCA PN Lock
Energy Detect RSSI
Data
Data O.H.
Data, Data O.H. Preamble
SYMBOL TRACKING / PN ACQUISITION
90
Symbol Tracking Timing
88
Cartesian To Polar
Integrate & Dump
MF Correlator 16-bits
PN Code
84
86
(Signal Quality Indicator)
Bit Sync
Signal Quality Estimate And Clear Channel Assessment
RSSI
Phase Error
(Signal Quality Indicator)
Mag., Phase
Phase
Bit Sync
DEMODULATOR
Data De-Scrambler
104
Diff. Decode
102
PSK Demod
100
AFC
108
MODULATOR
Data Scrambler
110
116
Prog. PN Code
Diff. Encode Sym
Iout
Qout
54
3-BIT ADC
82
LEVEL ADJUST
1
0
Adjust Ref
Ref-
Ref+
AGC out
Iin
56
3-BIT ADC
Qin
6-BIT ADC
RSSI
T/R

FIG. 3