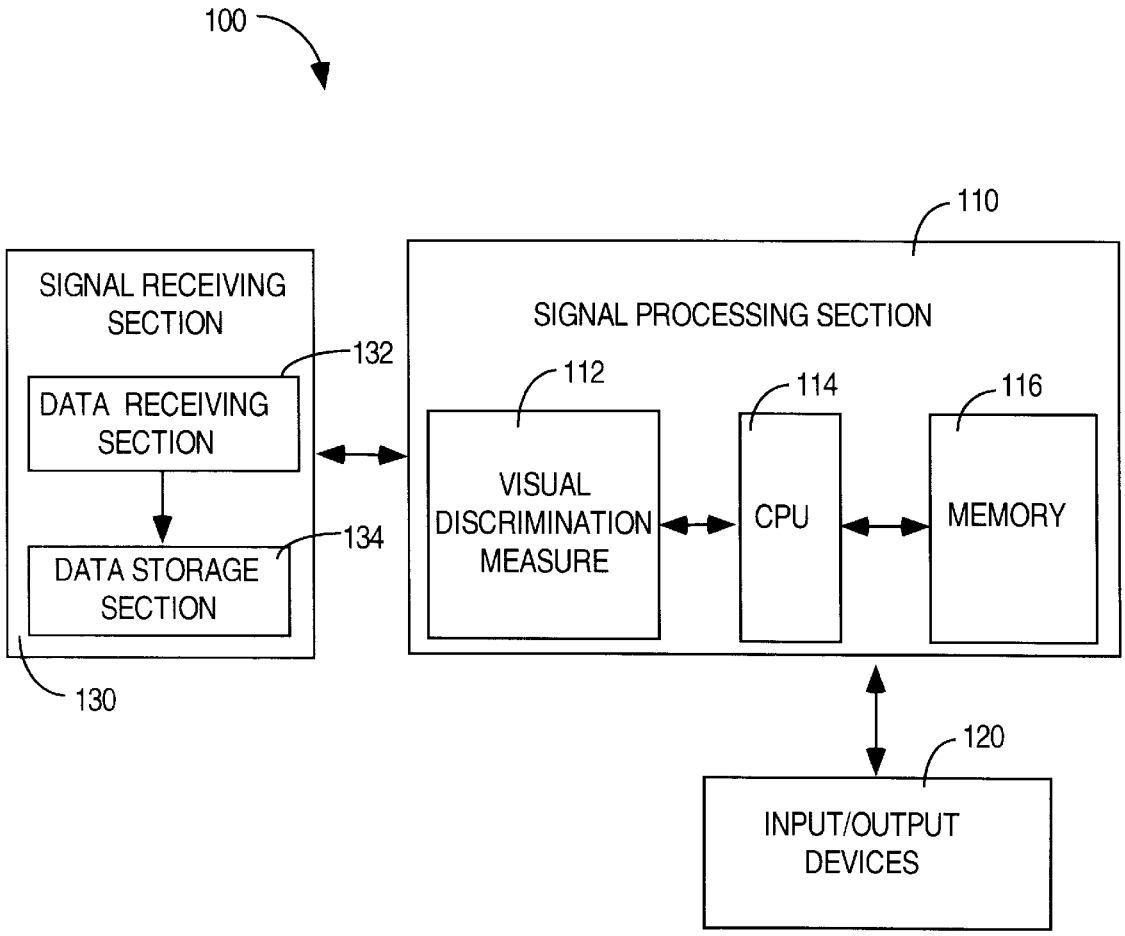
100
110
SIGNAL RECEIVING SECTION
SIGNAL PROCESSING SECTION
132
DATA RECEIVING SECTION
112
114
116
VISUAL DISCRIMINATION MEASURE
CPU
MEMORY
134
DATA STORAGE SECTION
130
120
INPUT/OUTPUT DEVICES

FIG. 1