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(54) **LOW-E PANELS AND METHODS FOR FORMING THE SAME**

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This patent is subject to a terminal disclaimer.

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(58) **Field of Classification Search**

USPC 428/426, 428, 432, 433, 434, 688, 689, 428/697, 699, 701, 702

See application file for complete search history.

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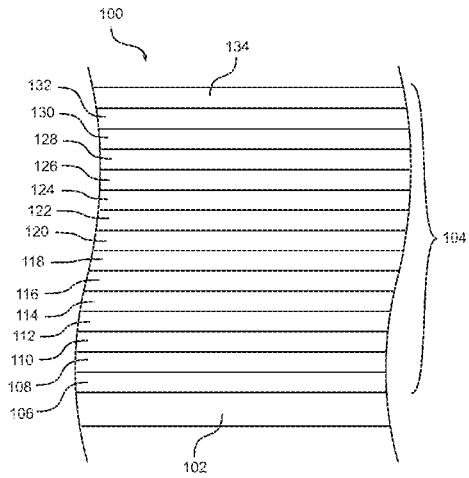
(57) **ABSTRACT**

Embodiments provided herein describe low-e panels and methods for forming low-e panels. A transparent substrate is provided. A low-e stack is formed above the transparent substrate. Each of the layers of the low-e stack are formed to have a specific thickness to tune the performance characteristics of the low-e panel.

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