11. The transmitter of claim 10, the second portion including OFDM symbols wherein each OFDM symbol includes a guard interval with a standard number of samples for OFDM.

12. The transmitter of claim 10, the second portion including OFDM symbols wherein each OFDM symbol includes a guard interval with an increased number of samples.

13. The transmitter of claim 10, the second portion including OFDM symbols wherein each OFDM symbol includes a reduced number of frequency subcarriers.

14. The transmitter of claim 13, wherein each OFDM symbol includes 48 frequency subcarriers.

15. The transmitter of claim 13, wherein each of the frequency subcarriers is a data subcarrier.

16. The transmitter of claim 13, wherein the frequency subcarriers include at least one pilot tone.

17. The transmitter of claim 13, wherein each of the frequency subcarriers initially comprises a data subcarrier; and the second modulator discards a subset of the data subcarriers and replaces the discarded data subcarriers with a corresponding number of pilot tones for transmission.

18. The transmitter according to claim 1, wherein the serial modulation comprises direct sequence spread spectrum (DSSS).

19. A wireless communication device that is configured to communicate using a dual packet configuration, comprising:

a transmitter configured to transmit packets, each with a dual configuration including first and second portions

a receiver configured to receive packets with a dual configuration, wherein the transmitter and receiver are each capable of communicating in a super short mode in which only a second portion modulated according to the parallel modulation is utilized; and

another mode in which, the first portion is modulated solely according to a serial modulation technique and the second portion is modulated solely according to a parallel modulation technique, wherein the parallel modulation technique is orthogonal frequency division multiplexing (OFDM).

20. The wireless communication device of claim 19, the first portion including a header with an OFDM mode bit.

21. The wireless communication device of claim 20, the header further including a length field indicating the duration of the second portion.

22. The wireless communication device of claim 19, further comprising:

a first clock source based on a first clock fundamental, the first portion based on the first clock fundamental; and

a second clock source based on a second clock fundamental, the second portion based on the second clock fundamental.

23. The wireless communication device of claim 22, wherein the first clock fundamental is approximately 22 Megahertz (MHz) and the second clock fundamental is approximately 20 MHz.

24. The wireless communication device of claim 19, further comprising:

a clock source based on a clock fundamental, the first and second portions based on the clock fundamental.

25. The wireless communication device of claim 24, wherein the second portion includes OFDM symbols, each OFDM symbol including a guard interval with a standard number of samples for OFDM.

26. The wireless communication device of claim 24, wherein the second portion includes OFDM symbols, each OFDM symbol including a guard interval with an increased number of samples.

27. The wireless communication device of claim 24, wherein the second portion includes OFDM symbols, each OFDM symbol including a reduced number of frequency subcarriers.

28. The wireless communication device of claim 27, wherein each of the frequency subcarriers is a data subcarrier.

29. The wireless communication device of claim 27, wherein the frequency subcarriers include at least one pilot tone.

30. The wireless communication device of claim 27, further comprising:

the transmitter discarding at least one of the data subcarriers and replacing the discarded data subcarriers with a corresponding number of pilot tones; and

the receiver regenerating the discarded data subcarriers based on received data subcarriers.

31. The wireless communication device of claim 19, wherein the transmitter and receiver are each capable of communicating in a standard mode in which the second portion is modulated according to the serial modulation.

32. The wireless communication device of claim 19, further comprising:

the transmitter and receiver each configured to operate in the 2.4 gigahertz frequency band.

33. The wireless communication device according to claim 19, wherein the serial modulation is direct sequence spread spectrum (DSSS).

34. A method of wireless communication using a dual packet configuration, comprising:

operating in a first mode, including modulating a first portion of each packet solely according to a serial modulation;

modulating a second portion of each packet according to a parallel modulation, wherein modulating a second portion of each packet comprises modulating the second portion according to orthogonal frequency division multiplexing (OFDM); and

switching to a super short mode of operation in which only the second portion modulated according to the parallel modulation is utilized for communications.

35. The method of claim 34, further comprising:

including a header with an OFDM mode bit in the first portion; and

including a length field in the header indicating a duration of the second portion.

36. The method of claim 34, further comprising:

the modulating a first portion of each packet comprising modulating based on a first clock fundamental; and

the modulating a second portion of each packet comprising modulating based on a second clock fundamental.

37. The method of claim 34, wherein the modulating first and second portions of each packet comprises modulating based on a single clock fundamental.

38. The method of claim 37, wherein the modulating the second portion of each packet comprises including a guard interval with a standard number of samples for each OFDM symbol.

39. The method of claim 37, wherein the modulating the second portion of each packet comprises including a guard interval with an increased number of samples for each OFDM symbol.