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Brown

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[54] **CONTROL FOR COMPUTER WINDOWING DISPLAY**

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[52] **U.S. Cl.** **395/157**; 345/119; 345/123

[58] **Field of Search** 395/157; 340/721, 724; 345/56, 115, 116, 118, 119, 121, 123, 124, 125

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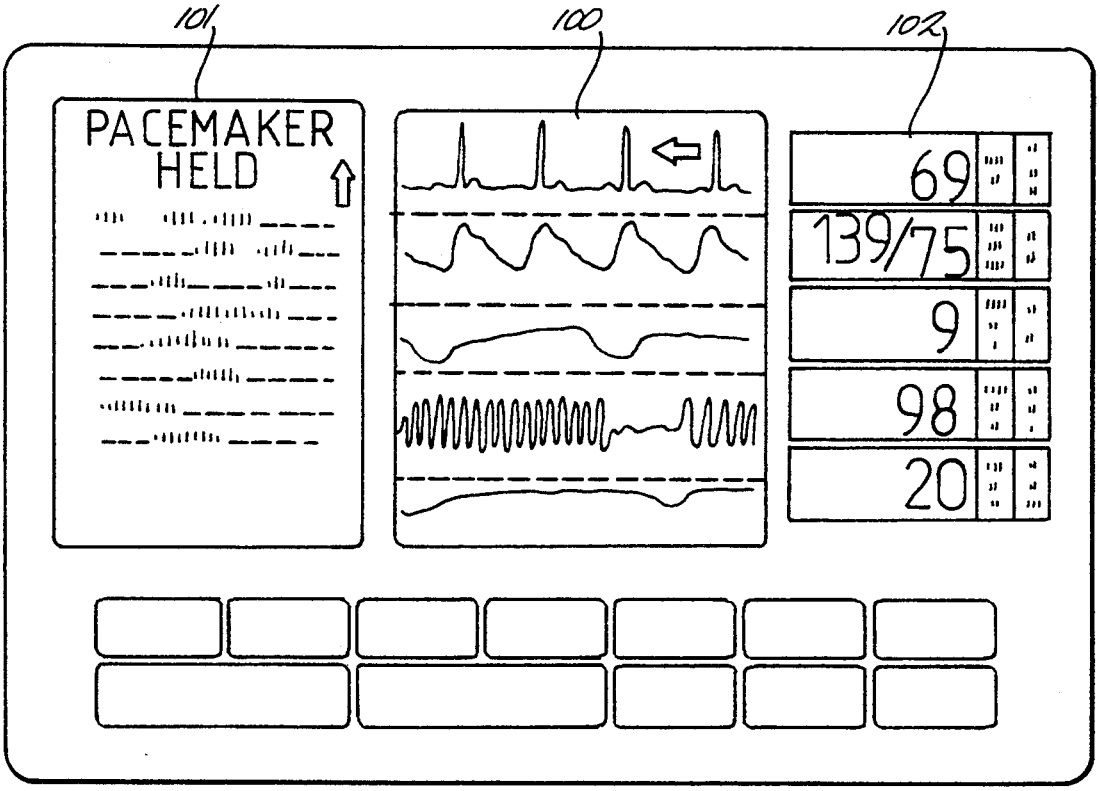
[57] **ABSTRACT**

A windowing control for a video display includes a programmed controller, a video display including a screen and a raster for scanning the screen, a first memory having a first area for storing data to be displayed in a first window defined on the screen and a second area for storing data to be displayed in a second window on the screen and a second memory for storing the addresses of the data to be displayed in the first and second windows and the transitions between the windows. The controller includes a processor programmed to load data into the first and second memory means and a memory controller for initiating the transfer of data from the first and second memory to a pixel multiplexer. The processor is also programmed to load new address data into the second memory means for each complete raster scan so that the position of the data within the window has the appearance of horizontal (panning) or vertical (scrolling) movement.

29 Claims, 5 Drawing Sheets