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Martisauskas

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(54) **REDUCING THE BORDER AREA OF A DEVICE**

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H05K 1/00 (2006.01)

(52) **U.S. Cl.**
USPC **361/749**; 361/679.56; 361/748; 174/250; 174/254

(58) **Field of Classification Search**
USPC 361/749, 679.56, 748; 174/254, 250
See application file for complete search history.

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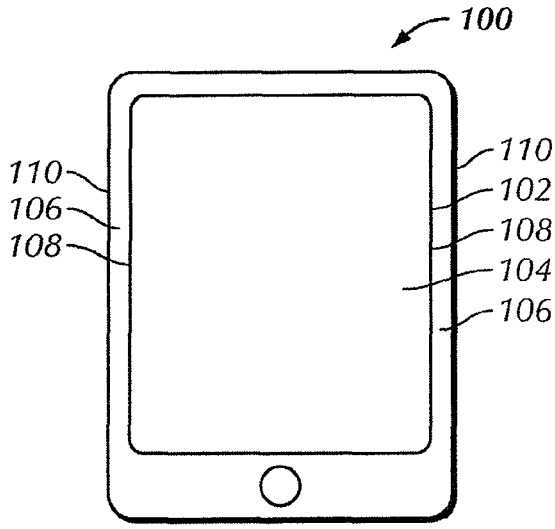
(57) **ABSTRACT**

A circuit panel of an electronic device is disclosed. The circuit panel includes a substantially flat surface including an active area of the electronic device; a bent border area contiguous with and extending from the active area of the substantially flat surface; and a plurality of traces coupled to the active area and routed in the bent border area.

18 Claims, 6 Drawing Sheets