a first power amplifier coupled to the first antenna element; and

a second power amplifier coupled to the second antenna element.

25. An antenna structure in accordance with claim **21**, wherein two of the at least two antenna elements are sufficiently spaced apart so as to achieve spatial diversity.

26. An antenna structure in accordance with claim **21**, wherein two of the at least two antenna elements are spaced apart by a distance equal to or greater than 0.5λ for a predetermined frequency of operation.

27. An antenna structure in accordance with claim **26**, wherein the predetermined frequency of operation falls within 5 to 6 gigahertz (GHz).

28. An antenna structure in accordance with claim **21**, wherein the card comprises connectors located at a second end thereof configured for engagement with an interface slot.

29. An antenna structure in accordance with claim **21**, wherein at least one antenna element is located on a first surface of the card and at least one antenna element is located on a second surface of the card.

30. An antenna structure in accordance with claim **21**, wherein one or more of the at least two antenna elements comprises a patch antenna.

31. An antenna structure in accordance with claim **21**, wherein one or more of the at least two antenna elements comprises a monopole antenna.

32. An antenna structure in accordance with claim **21**, wherein one or more of the at least two antenna elements comprises a vertically polarized antenna.

33. An antenna structure in accordance with claim **21**, wherein one or more of the at least two antenna elements comprises a horizontally polarized antenna.

34. An antenna structure in accordance with claim **21**, wherein the at least two antenna elements comprise four antenna elements.

35. An antenna structure in accordance with claim **34**, wherein three of the antenna elements are located on a first surface of the card and one of the antenna elements is located on a second surface of the card.

36. An antenna structure in accordance with claim **34**, wherein all four of the antenna elements comprise patch antennas.

37. An antenna structure in accordance with claim **34**, wherein two of the antenna elements comprise patch antennas and two of the antenna elements comprise monopole antennas.

38. An antenna structure in accordance with claim **21**, wherein the at least two antenna elements comprise six antenna elements.

39. An antenna structure in accordance with claim **38**, wherein three of the antenna elements are located on a first surface of the card and three of the antenna elements are located on a second surface of the card.

40. An antenna structure in accordance with claim **38**, wherein two of the antenna elements comprise patch antennas and four of the antenna elements comprise monopole antennas.

41. A method of receiving a signal in a multi-path environment, comprising the steps of:

placing a card configured for insertion into a slot of a device in the multi-path environment, the card having active circuitry attached thereto;

receiving the signal with a first antenna element attached to the card at a first end thereof; and

receiving the signal with a second antenna element attached to the card at the first end thereof;

wherein the first and second antenna elements are coupled to the active circuitry and are located and configured to provide at least one antenna gain pattern that provides at least some coverage on one side of the card and at least one antenna gain pattern that provides at least some coverage on another side of the card so that at least some coverage is provided on both sides of the card.

42. A method in accordance with claim **41**, wherein the first and second antenna elements are sufficiently spaced apart so as to achieve spatial diversity.

43. A method in accordance with claim **41**, wherein the first and second antenna elements are spaced apart by a distance equal to or greater than 0.5λ for a predetermined frequency of operation.

44. A method in accordance with claim **43**, wherein the predetermined frequency of operation falls within 5 to 6 gigahertz (GHz).

45. A method in accordance with claim **41**, wherein the first antenna element comprises a polarization that is orthogonal to a polarization of the second antenna element so as to achieve polarization diversity.

46. A method in accordance with claim **45**, wherein the active circuitry comprises;

a first power amplifier coupled to the first antenna element; and

a second power amplifier coupled to the second antenna element.

47. A method in accordance with claim **41**, wherein the card comprises connectors located at a second end thereof configured for engagement with an interface slot.

48. A method in accordance with claim **41**, wherein the first antenna element is located on a first surface of the card and the second antenna element is located on a second surface of the card.

49. A method of transmitting a signal in a multi-path environment, comprising the steps of:

placing a card configured for insertion into a slot of a device in the multi-path environment, the card having active circuitry attached thereto;

transmitting the signal with a first antenna element attached to the card at a first end thereof; and

transmitting the signal with a second antenna element attached to the card at the first end thereof;

wherein the first and second antenna elements are coupled to the active circuitry and are located and configured to provide at least one antenna gain pattern that provides at least some coverage on one side of the card and at least one antenna gain pattern that provides at least some coverage on another side of the card so that at least some coverage is provided on both sides of the card.

50. A method in accordance with claim **49**, wherein the first and second antenna elements are sufficiently spaced apart so as to achieve spatial diversity.