

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,532,537 B2
APPLICATION NO. : 11/335875
DATED : May 12, 2009
INVENTOR(S) : Jeffrey C. Solomon and Jayesh R. Bhakta

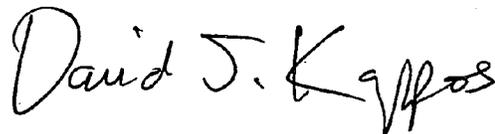
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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

- At page 2, column 1, line 49, Change "fourbanks," to --four banks--.
- At page 5, column 3, line 52, Change "the, computer" to --the computer--.
- At page 9, column 6, line 30, Change "30a30b" to --30a, 30b--.
- At page 20, column 15, line 13, Change "plurality-of" to --plurality of--.
- At page 23, column 17, line 3, Change "(CS₀-CS₁)" to --(CS₀, CS₁)--.
- At page 41, column 33, line 25-26, Change "128M×8-bit-memory" to --128M×8-bit memory--.
- At page 45, column 36, line 2, Change "are, enabled" to --are enabled--.
- At page 45, column 36, line 2, Change "(egg,," to --(e.g.,--.
- At page 47, column 37, line 23, Change "are, electrically" to --are electrically--.
- At page 5, column 42, line 49, In Claim 18, change "first, number" to --first number--.

Signed and Sealed this

Sixteenth Day of March, 2010



David J. Kappos
Director of the United States Patent and Trademark Office