U.S. PATENT DOCUMENTS

6,070,217	A	5/2000	Connolly et al.	
6,154,418	A	11/2000	Li	
6,226,709	B1	5/2001	Goodwin et al.	
6,453,381	B1	9/2002	Yuan et al.	
6,518,794	B2 *	2/2003	Coteus et al.	326/93
6,553,450	B1	4/2003	Dodd et al.	
6,681,301	B1	1/2004	Mehta et al.	
6,742,098	B1 *	5/2004	Halbert et al.	711/172
6,785,189	B2	8/2004	Jacobs et al.	
6,807,125	B2	10/2004	Coteus et al.	
6,813,196	B2	11/2004	Park et al.	
6,944,694	B2	9/2005	Pax	
6,981,089	B2	12/2005	Dodd	
6,982,893	B2	1/2006	Jakobs	
6,996,686	B2	2/2006	Doblar et al.	
7,007,175	B2 *	2/2006	Chang et al.	713/300
7,120,727	B2	10/2006	Lee et al.	
7,133,972	B2 *	11/2006	Jeddeloh	711/137
7,289,386	B2 *	10/2007	Bhakta et al.	365/230.06
2001/0003198	A1	6/2001	Wu	
2001/0052057	A1	12/2001	Lai et al.	
2002/0088633	A1	7/2002	Kong et al.	
2003/0063514	A1	4/2003	Faue	
2003/0191995	A1	10/2003	Abrosimov et al.	
2003/0210575	A1	11/2003	Seo et al.	
2004/0037158	A1	2/2004	Coteus et al.	
2005/0036378	A1	2/2005	Kawaguchi et al.	
2005/0281096	A1	12/2005	Bhakta et al.	
2006/0117152	A1	6/2006	Amidi et al.	
2006/0126369	A1	6/2006	Raghuram	
2006/0129755	A1	6/2006	Raghuram	

OTHER PUBLICATIONS

Miles J. Murdocca et al., "Principles of Computer Architecture", Prentice Hall, 2000, pp. 249-251.

"64 & 72 Pin Zip/Simm Sram Module", Jedec, Standard No. 21-C, www.jedec.com/download/search/4_04_01.pdf, Jun. 1997 pp. 4.4.1-1.

"PC2100 and PC1600 DDR SDRAM Registered DIMM Design Specification" Jedec, Standard No. 21-C, Revision 1-3, Jan. 2002, pp. 4.20.4-1.

Cuppu, Vinodh, et al., "A Performance Comparison of Contemporary DRAM Architectures," *IEEE Proceedings of the 26th International Symposium on Computer Architecture*, May 2-4, 1999, Atlanta, Georgia, pp. 1-12.

Jedec Standard No. 21-C, 4.20-2—168 Pin, PC133 SDRAM Registered Design Specification, Revision 1.4, Release 11a, Feb. 2002.

Jedec Standard No. 21-C, 4.20-3—144 Pin. PC133 SDRAM Unbuffered SO-DIMM, Reference Design Specification, Revision 1.02, Release 11, Published Oct. 2003.

Jedec Standard No. 21-C, DDR SDRAM PC2100 and PC1600 DDR SDRAM Registered DIMM Design Specification, Revision 1.3, Release 11b, Jan. 2002.

Jedec Standard No. 21-C, 4.20.5—184 Pin. PC1600/2100 DDR SDRAM Unbuffered DIMM Design Specification, Revision 1.1, Release 11b, Published Apr. 2003.

Jedec Standard No. 21-C, 4.20.6—200 Pin, PC2700/PC2100/PC1600 DDR SDRAM Unbuffered SO-DIMM Reference Design Specification, Revision 1.1, Release 11b, Apr. 26, 2002.

Jedec standard JESD79D, "Double Data Rate (DDR) SDRAM Specification," published Feb. 2004.

Intel Corporation, PC SDRAM Registered DIMM Design Support Document, Revision 1.2, Oct. 1998.

Intel Corporation, 66/100 MHz PC SDRAM 64-Bit Non-ECC/Parity 144 Pin Unbuffered SO-DIMM Specification, Revision 1.0, Feb. 1999.

* cited by examiner