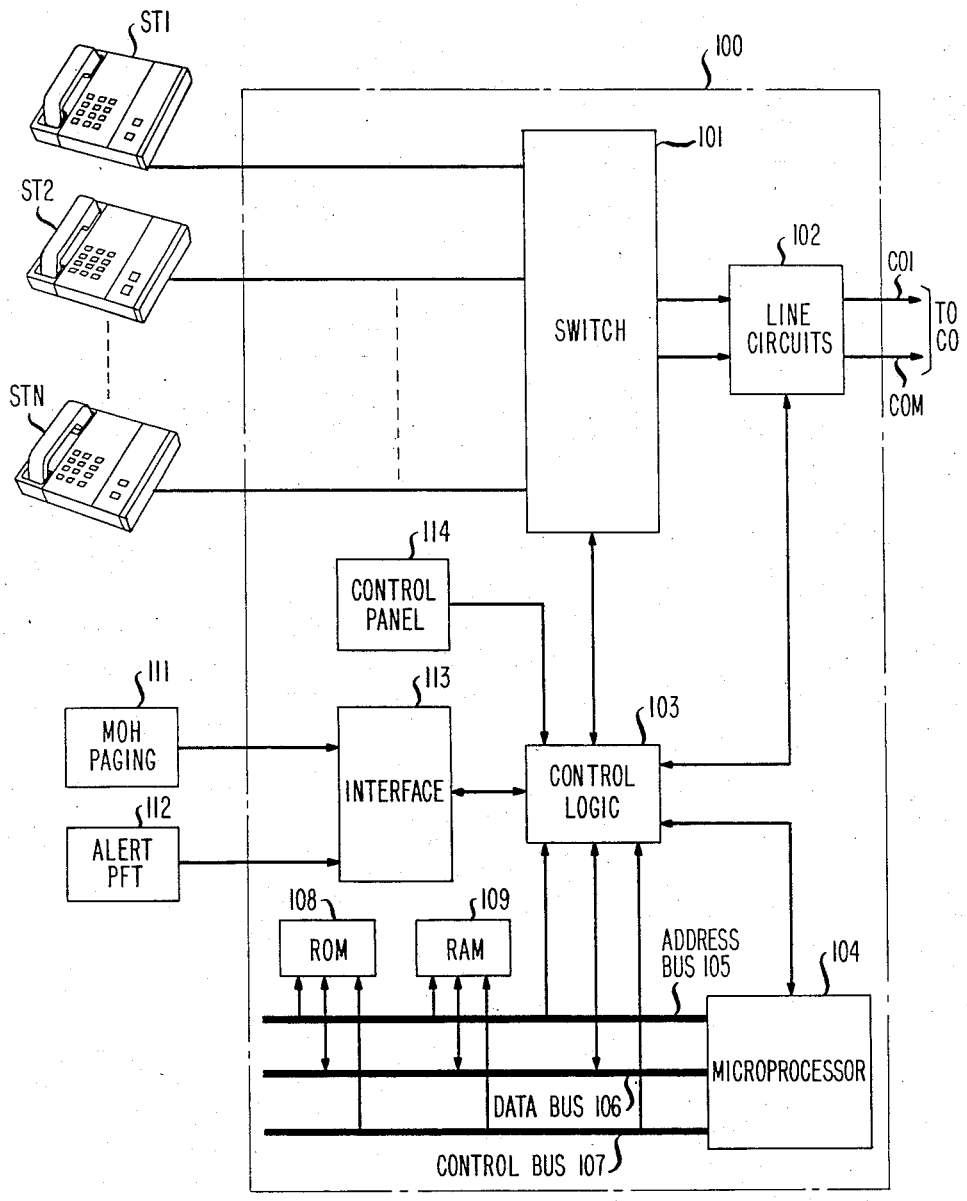
FIG. 1


ST1
ST2
STN
100
101
SWITCH
102
LINE CIRCUITS
CO1
TO CO
COM
114
CONTROL PANEL
111
MOH PAGING
112
ALERT PFT
113
INTERFACE
103
CONTROL LOGIC
108
ROM
109
RAM
ADDRESS BUS 105
104
MICROPROCESSOR
DATA BUS 106
CONTROL BUS 107