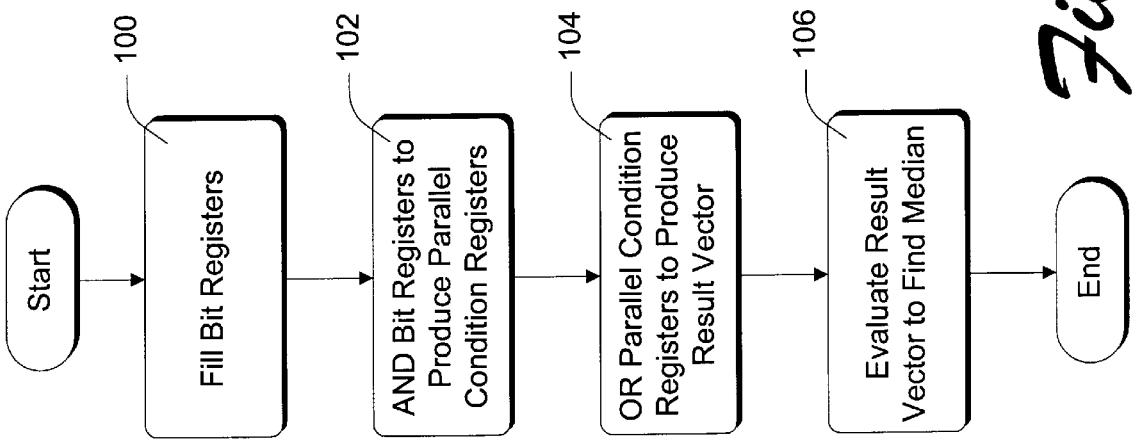
Start
100
Fill Bit Registers
102
AND Bit Registers to Produce Parallel Condition Registers
104
OR Parallel Condition Registers to Produce Result Vector
106
Evaluate Result Vector to Find Median
End

Fig. 6