35. A computer as recited in claim 33, further comprising a result register having bit groups corresponding respectively to the different sets of pixel values, each bit group having bit positions corresponding respectively do different individual ones of the pixels of the corresponding set, wherein the processor evaluates the parallel condition registers by logically ORing the parallel condition registers and placing the results in the result register, wherein a true value at any particular bit position of the result register indicates that the pixel corresponding to that particular bit position is the median pixel of its set, said logical ORing being performed as part of a single-instruction/multiple-data operation.

36. A computer as recited in claim 33, further comprising a result register having bit groups corresponding respectively to the different sets of pixel values, each bit group having bit positions corresponding respectively do different individual ones of the pixels of the corresponding set, wherein the processor evaluates the parallel condition registers by logically ORing the parallel condition registers and placing the results in the result register, wherein a true value at any particular bit position of the result register indicates that the pixel corresponding to that particular bit position is the median pixel of its set, said logical ORing being performed as part of a single-instruction/multiple-data operation; the processor being further programmed to index a lookup table with the result of the logical ORing to find median pixel values.

37. A computer as recited in claim 33, wherein the processor evaluates the parallel condition registers by logically ORing the parallel condition registers to produce a result vector having bit groups corresponding respectively to the different sets of pixel values, each bit group having bit positions corresponding respectively do different individual ones of the pixels of the corresponding set;

wherein a true value at any particular bit position of the result vector indicates that the pixel corresponding to that particular bit position is the median pixel of its set;

said logical ORing being performed as part of a single-instruction/multiple-data operation.

38. A computer as recited in claim 33, wherein each bit of the bit registers indicates whether one of the pixels is greater than another of the pixels.

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