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10. The apparatus of claim 9 further comprises a circuit responsive to a request from an interface for removing a sample from the queue and transmitting the sample to the interface.

11. The apparatus of claim 10 wherein the circuit further responsive to the request for decrementing the counter upon the removed sample being a low energy sample.

12. The apparatus of claim 10 wherein the circuit further determining the number of samples in the queue and deleting a next low energy sample from the queue upon the queue containing more than a maximum number of samples for the queue and decrementing the counter upon the next low energy sample being deleted.

13. The apparatus of claim 9 further comprises a circuit responsive to a request from an interface for removing a sample from the queue and transmitting the sample to the interface and maintaining a count of a number of non-low energy samples transmitted to the interface since the last low energy sample was transmitted to the interface.

14. The apparatus of claim 13 wherein the circuit further responsive to the request from the interface for decrementing the counter upon the removed sample being a low energy sample.

15. The apparatus of claim 14 wherein the circuit further determining if a next sample from the queue is a low energy sample;

the circuit determining if number of samples in the queue is greater than a first predefined value;

the circuit determining if the count of low energy samples is less than a first predefined counter value;

the circuit determining if the count of non-low energy samples is greater than a first predefined count value; and

the circuit deleting the next sample from the queue upon the next sample from the queue being a low energy

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sample, the number of samples in the queue being greater than the first predefined value, the counter being less than the first predefined counter value and the count of non-low energy samples being greater than the first predefined count value.

16. The apparatus of claim 15 further comprises the steps of decrementing the counter; and

setting the count of non-low energy samples equal to zero.

17. The apparatus of claim 14 further the circuit determining if the next sample from the queue is a low energy sample;

the circuit determining if number of samples in the queue is greater than a first predefined value;

the circuit determining if the counter is greater than a first predefined counter value and less than a second predefined counter value.

the circuit determining if the count of non-low energy samples is greater than a first predefined count value and less than a second predefined count value; and

the circuit deleting the next sample from the queue upon the next sample from the queue being a low energy sample, the number of samples in the queue being greater than the first predefined value, the counter being greater than the first predefined counter value and less than the second predefined counter value and the count of non-low energy samples being greater than the first predefined count value and less than the second predefined count value.

18. The apparatus of claim 17 wherein the circuit further decrementing the counter and setting the count of non-low energy samples equal to zero.

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