United States Patent [19]

Kaufman et al.

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[54] **AUTOMATIC MODULAR MEMORY ADDRESS ALLOCATION SYSTEM**

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[51] **Int. Cl.**[2] **G06F 13/06**; G11C 5/06; G11C 7/00; G11C 9/00

[58] **Field of Search** 340/172.5; 444/1; 445/1

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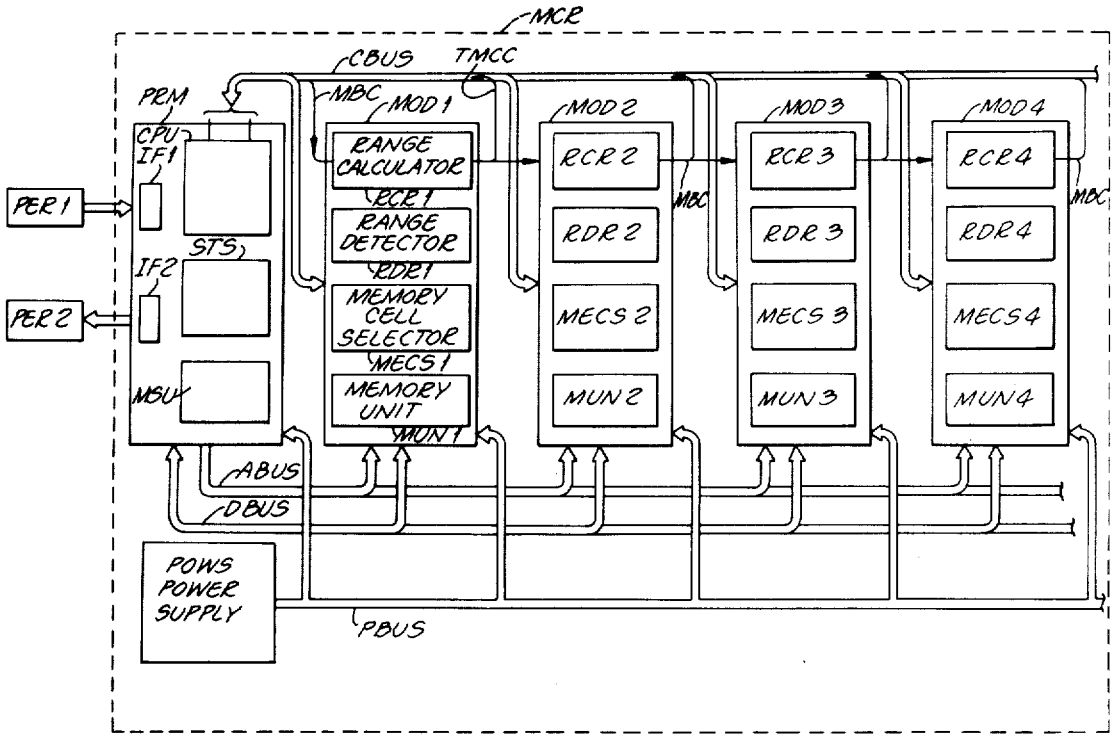
[57] **ABSTRACT**

A modular minicomputer is provided which is assembled from a central processor unit module and a plurality of memory modules. Small calculators on the memory modules are so interlocked that when the computer is powered up, memory address boundaries are calculated automatically. As a result, the bank of memory modules appears to the central processing unit the same as a single large memory unit.

43 Claims, 10 Drawing Figures