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(12) United States Design Patent

Cho et al.

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(54) **ELECTRONIC DEVICE**

(71) Applicant: **Samsung Electronics Co., Ltd.**, Suwon-si, Gyeonggi-do (KR)

(72) Inventors: **Chul-Yong Cho**, Seoul (KR); **Dae Kyung Ahn**, Seoul (KR); **Sangmin Hyun**, Seoul (KR); **Eun Vit Chung**, Seoul (KR)

(73) Assignee: **SAMSUNG ELECTRONICS CO., LTD.** (KR)

(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

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(52) **U.S. Cl.**
USPC **D14/341**

(58) **Field of Classification Search**
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CPC A63H 33/3016; G06F 1/1613; H04M 1/0202; H04M 1/0279; H04M 1/0281; H04M 1/0283
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D425,558	S	*	5/2000	Tarpenning et al.	D19/26
D649,966	S	*	12/2011	Chiu	D14/341
D656,137	S	*	3/2012	Chung et al.	D14/341
D665,799	S	*	8/2012	Massaro et al.	D14/315
D675,613	S	*	2/2013	Chu et al.	D14/341
D680,529	S	*	4/2013	Reeves et al.	D14/341
D681,054	S	*	4/2013	Koh et al.	D14/496

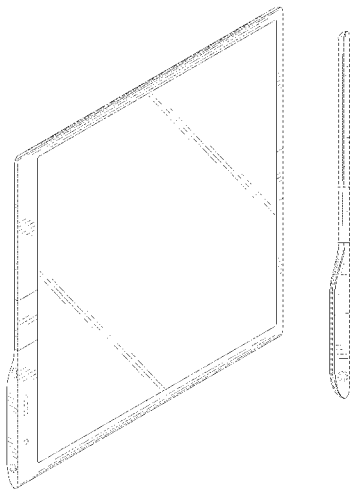
(Continued)

Primary Examiner — Barbara Fox
Assistant Examiner — Dana K Weiland
(74) *Attorney, Agent, or Firm* — Cantor Colburn LLP

(57) **CLAIM**

The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. **1** is the perspective view of an electronic device showing my new design;
FIG. **2** is the front view thereof;
FIG. **3** is the rear view thereof;
FIG. **4** is the left-side view thereof;
FIG. **5** is the right-side view thereof;
FIG. **6** is the top plan view thereof;
FIG. **7** is the bottom plan view thereof;
FIG. **8** is a perspective view thereof, with the electronic device being folded;
FIG. **9** is a left side view thereof, with the electronic device being folded; and,
FIG. **10** is a back perspective view thereof, with the stand popped out.
The broken lines as shown in the drawings represent portions of the electronic device that form no part of the claimed design.
The shade lines in the figures show contour and not surface ornamentation.
References to "side", "top", and "bottom" in the figure descriptions are not meant to require certain in-use orientation; an electronic device according to the claimed design may be used in any orientation.

1 Claim, 9 Drawing Sheets