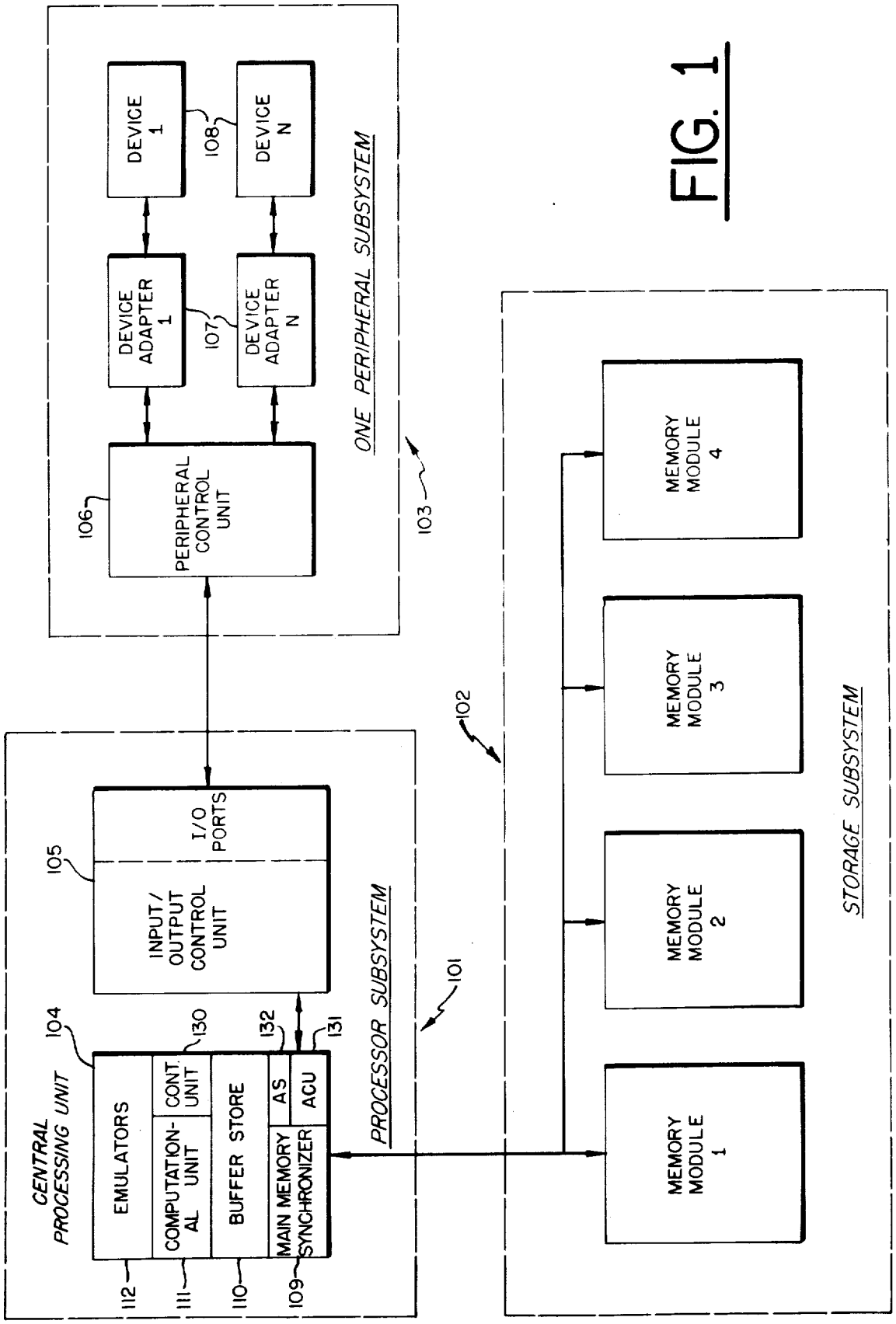
DEVICE 1
DEVICE N
108
DEVICE ADAPTER 1
DEVICE ADAPTER N
107
PERIPHERAL CONTROL UNIT
106
ONE PERIPHERAL SUBSYSTEM
103
102
MEMORY MODULE 4
MEMORY MODULE 3
MEMORY MODULE 2
MEMORY MODULE 1
STORAGE SUBSYSTEM
I/O PORTS
INPUT / OUTPUT CONTROL UNIT
105
PROCESSOR SUBSYSTEM
101
CENTRAL PROCESSING UNIT
104
EMULATORS
COMPUTATION-AL UNIT
CONT. UNIT
130
BUFFER STORE
MAIN MEMORY SYNCHRONIZER
AS
ACU
132
131
112
111
110
109

FIG. 1