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4, wherein said memory means coupled to said second processor includes a switched bank memory device.

7. A data processing system for monitoring the performance of a selected software application with a first processor having an output bus according to claim 6, further including disk storage means coupled to said second processor.

8. A data processing system for monitoring the performance of a selected software application within a first processor having an output bus according to claim 4, wherein said means for coupling said identification and said preselected data frame to said second processor comprises a data output card coupled to said output bus of said first processor.

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9. A data processing system for monitoring the performance of a selected software application within a first processor having an output bus according to claim 8, wherein said means for coupling said identification and said preselected data frame to said second processor comprises a data collection card coupled to said data output card by means of a dedicated cable.

10. A data processing system for monitoring the performance of a selected software application within a first processor having an output bus according to claim 8, wherein said second processor includes an input/output bus and wherein said data collection card is coupled to said input/output bus.

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